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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. .... 09/502,693  
Confirmation No. .... 5536  
Filing Date .... February 11, 2000  
Inventors .... James E. O'Toole et al.  
Assignee .... Micron Technology, Inc.  
Group Art Unit .... 2684  
Examiner .... Philip J. Sobutka  
Attorney's Docket No. .... MI40-273  
Title: Method of Speeding Power-Up of an Amplifier, and Amplifier

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450  
Attention: Official Draftsman

SUBSTITUTE DRAWING REQUEST


Please enter the enclosed substitute drawings in the above-referenced application in place of drawings originally filed. Red-lined drawings of Fig. 48 (now Fig. 1) and Fig. 49 (now Fig. 2) are being concurrently submitted with the filing of the Response to the Office Action of July 1, 2004.

The drawing corrections were required by Examiner Philip Sobutka in the Office Action dated July 1, 2004, by editing to the receiver structure in order to make the application and any resulting patent more manageable.

Acknowledgment of receipt of the formal drawings and their acceptance into the file is requested.

Respectfully submitted,

Date: Sept. 29, 2004

By:   
Deepak Malhotra  
Reg. No.: 33,560

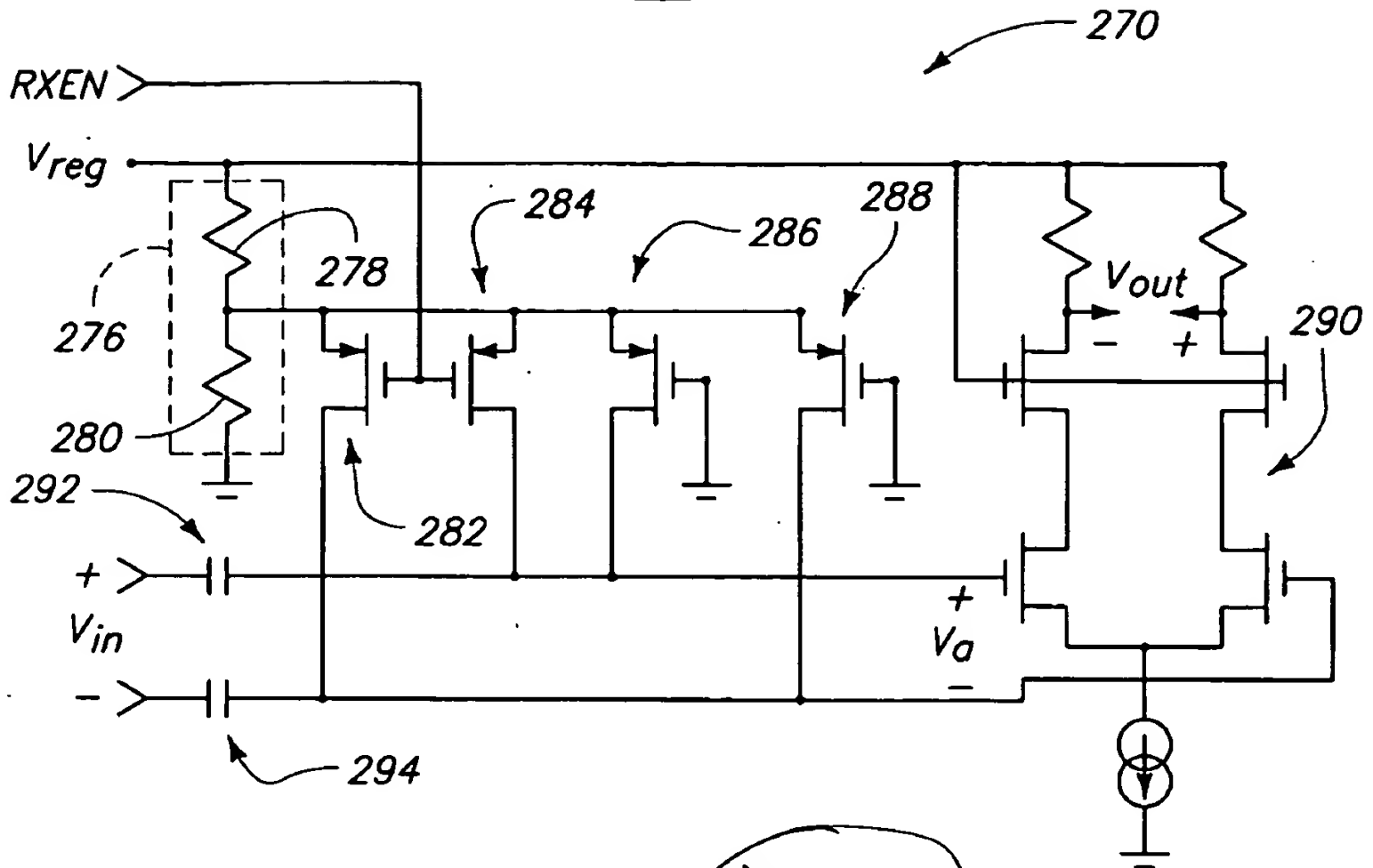
Enclosures: Sheet 1/1 of Formal Drawings, Figs. 1 and 2; Red-lined drawings



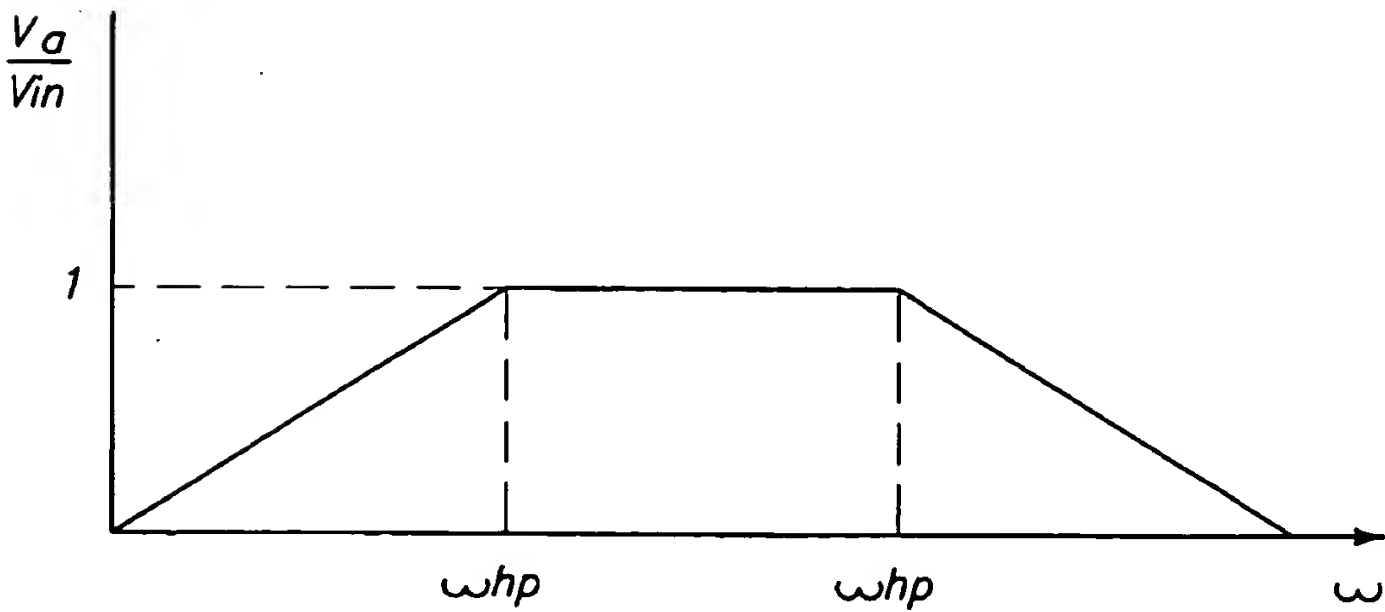
ANNOTATED SHEET SHOWING CHANGES

~~3253/3273~~

1/1



~~FIG. 1~~

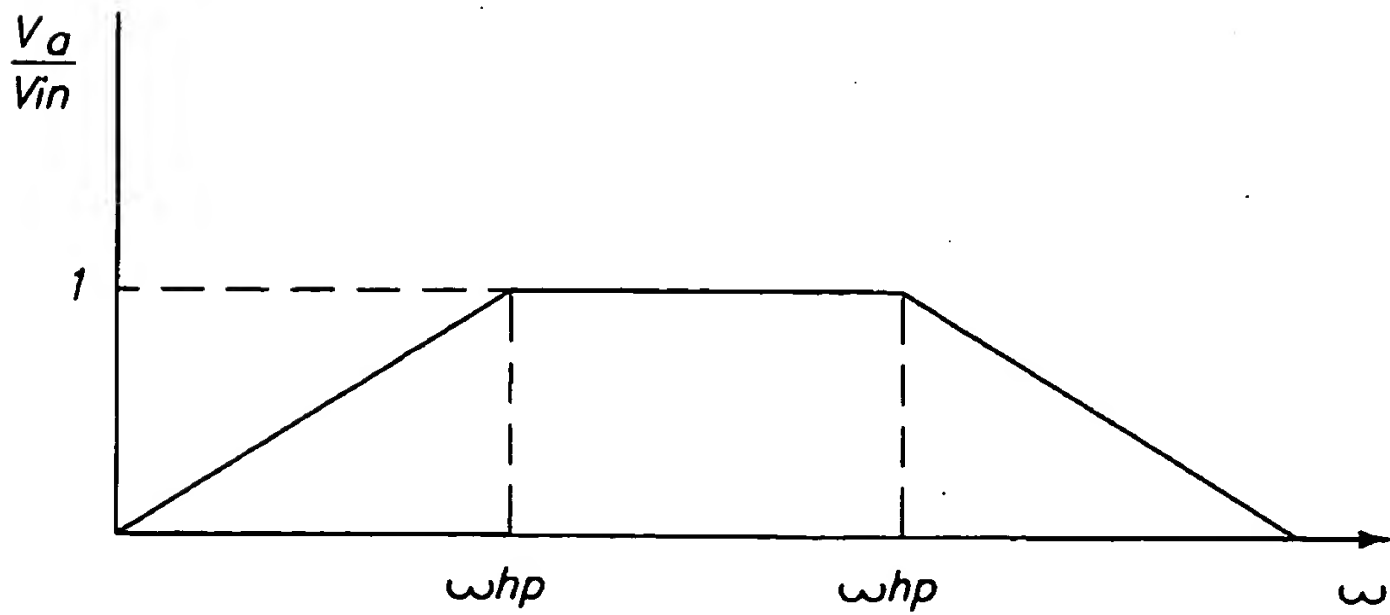
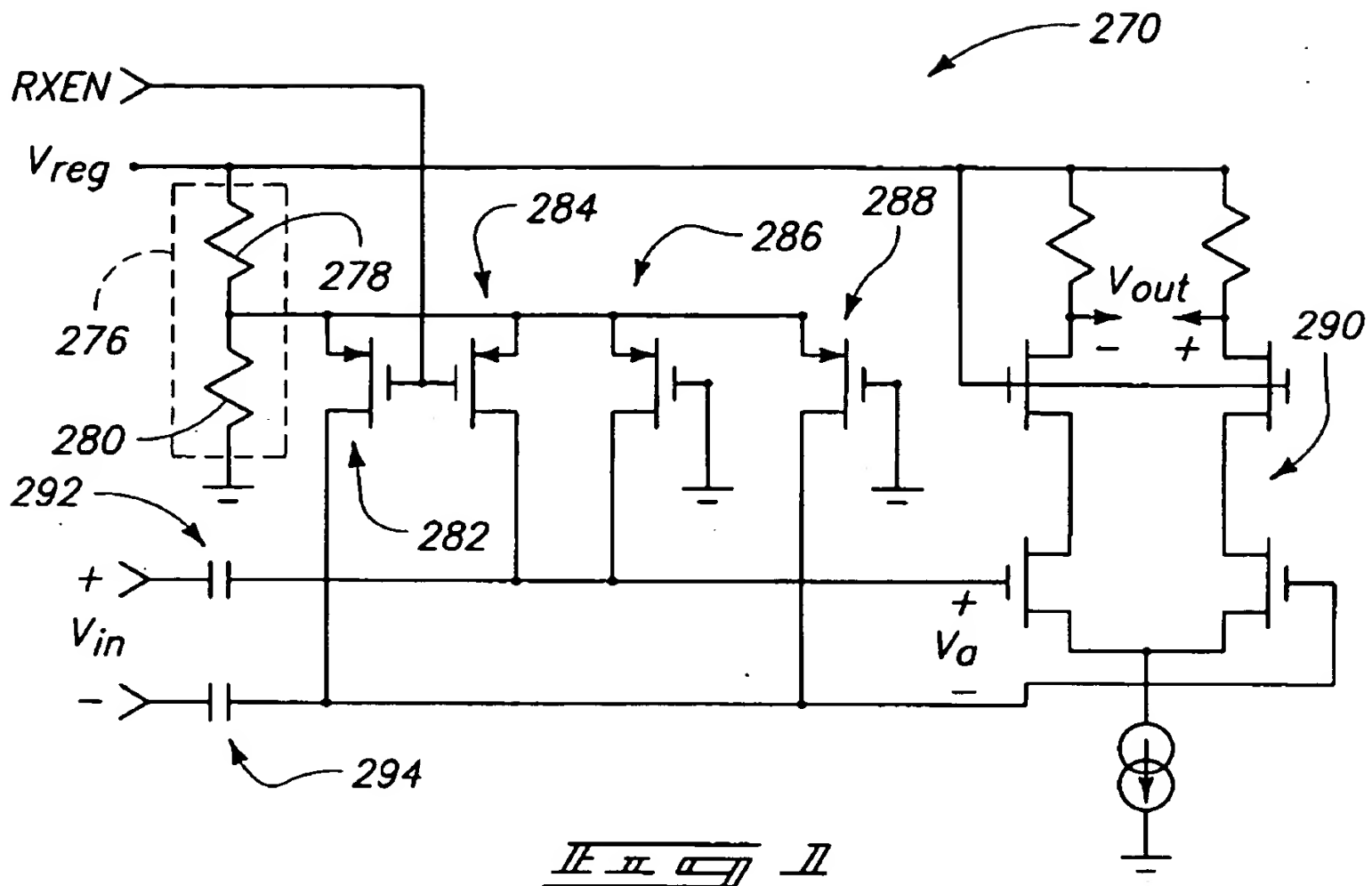


~~FIG. 2~~



REPLACEMENT SHEET

1/1



$\frac{V_a}{V_{in}}$